



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of

Tetsuya NISHIYAMA et al.

Group Art Unit: 2817

Application No.: 10/026,633

Examiner: Arnold M. Kinkad

Filed: December 27, 2001

Docket No.: 111598

For: OSCILLATION CIRCUIT WITH VOLTAGE-CONTROLLED OSCILLATORS

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

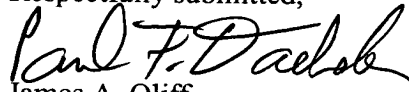
Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, Notice of Allowance or other action that closes prosecution (e.g., Quayle Action).
- a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
- ☒ 2. Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and this communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

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- ☒ 3. The references were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.

Respectfully submitted,



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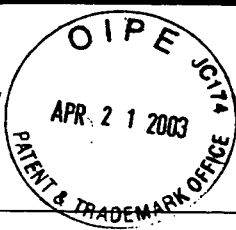
Date: April 21, 2003

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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)
Tetsuya NISHIYAMA et al.FILING DATE
December 27, 2001GROUP
2817

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		5,912,596	06/15/1999	GHOSHAL		

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		T. WAKIMOTO and S. KONAKA, "A 1.9-GHz Si Bipolar Quadrature VCO with Fully-Integrated LC Tank, 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 30-31
		R. DUNCAN et al., "A 1 GHz Quadrature Sinusoidal Oscillator, IEEE 1995 Custom Integrated Circuits Conference, pp. 91-94
		Chung-Yu WU and Hong-Sing KAO, "A 1.8GHz CMOS Quadrature Voltage-Controlled Oscillator (VCO) Using the Constant-Current LC Ring Oscillator Structure, 1998 IEEE, pp. IV-378-IV-381

EXAMINER

DATE CONSIDERED

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: April 21, 2003